IC600BF843 New In Stock! GE Fanuc

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Ge Series Six 6 1-919-535-3180

In Stock! 4-20mA Analog Input Module (8 channels) IC600B IC600BF

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Table 2. Specifications

Dimensions: Circuit Board:

Faceplate:

8.15 x 11.0 x 1.20 (inches), 208 x 280 x 31 (mm) 12.46 x 1.175 (inches), 317 x 30 (mm)

Operating Temperature: Storage Temperature:

0 to 60°C (at the outside of the rack)

Humidity:

-20 to **40**°C

5% - 95% (non-condensing)

Accuracy:

Voltage Input

+ 0.025% of full scale at 25°C typical

+ 0.050% of full scale at 25°C maximum

Current Input (4-20 mA)

+ 0.075% of full scale at 25°C typical (see note below)

+ 0.125% of full scale at 25°C maximum

NOTE

Any one input can be calibrated to within 0.025% of full scale at 25°C.

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Current Input (10-50 mA) with external 100 ohm shunt voltage input function (1-5 V) being used.

Therefore:+ 0.025% of full scale at 25°C typical PLUS

tolerance of 100 ohm shunt,

+ 0.050% of full scale at 25°C maximum <u>PLUS</u> tolerance of 100 ohm shunt.

Temperature Coefficient:

Linearity Gain offset

< 6ppm* of Full Scale per degree C

< 18ppm of Full Scale per degree C

< 10ppm of Full Scale per degree C
*ppm = parts per million</pre>

Example: 10 ppm = 10 / 1 million = .001%

Power Requirements:

Input Overvoltage:

Input Bias Currents:

5 V dc, 1.5 A, Supplied by I/O-rack power supply.

The user must supply analog input voltage or current levels. Differential or common mode transients up to 30 V will not

cause damage.

 $< 200 \ pA \ at +25 \ degrees \ C \ maximum$

< 8 nA at +70 degrees C maximum

Input Impedance:

Common Mode Voltage Operating Range:

Common Mode Rejection (Noise

Immunity):

 $< 100 \text{ M} \Omega \text{ Typical}$

+ 11 V maximum from inputs to SHD

>60dB, DC to lkHz

Cross Talk:

Resolution:

Input Filter:

>74dB at lkHz

12 Binary Bits (1 part in 4096)

Time Constant (nominal) is 1 milli-second

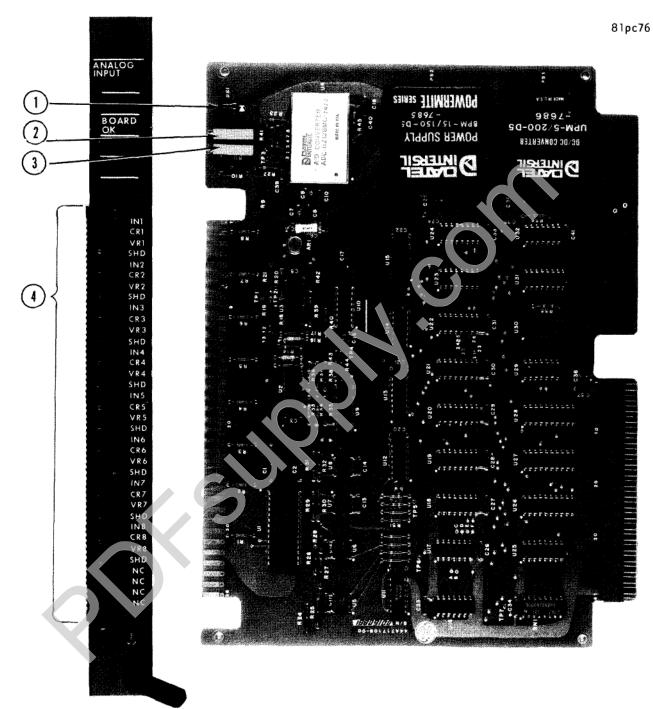


Figure 1. User Items

1. BOARD OK Light:

The LED is OFF if there is an A/D converter malfunction, an I/O-rack power supply problem, or the CPU is in the Stop or the Run Disabled mode. It is also Off if the module has not been read since one of these conditions existed, or since power has been applied.

- 2. R12: Offset Potentiometer, (All Channels)
- 3. R11: Gain Potentiometer, (All Channels)
- 4. User Connector Block

Installation

The Analog Input module can be installed in an I/O rack, a Model 60 CPU rack, or a Series Six Plus CPU rack. Before installing the Analog Input module, the dual-in-line-package (DIP) switches immediately behind the card slot on the rack backplane should be set to reserve a group of 32 consecutive bits in the appropriate Input Status Table of the CPU. For specific DIP switch settings, see below.

Use the extraction/insertion tool furnished with the CPU to remove or install the circuit board. With the board in place in the rack, the edge connector on the faceplate should be slipped over the circuit board so that proper contact is made, then secure the faceplate to the rack using the thumbscrews at the top and bottom. Both the Input terminal (IN) and the Return terminal of any unused input channels should be connected to the shield terminal.

Input Number	Dip Switch Position										
	7	6	5	4	3						
1 - 32											
33 - 64					X						
65 - 96				X							
97 - 128				X	X						
129 - 160			X								
161 - 192			X		X						
193 - 224			Х	Х							
225 -256			Х	X	X						
257 - 288		X									
289 - 320		X			X						
321 - 352		Х		x							

Input Number	Dip Switch Position										
	7	6	5	4	3						
353 - 384		X		X	X						
395 - 416		X	X								
417 - 448		X	X		X						
449 - 480		X	X	X							
481 - 512		X	\mathbf{x}	X	X						
513 - 544	X										
545 - 576	X				Х						
577 - 608	X			X							
609 - 640	х			X	Х						
641 - 672	Х		X								
673 - 704	Х		X		Х						

Input Number	Dip Switch Position										
	7	6	5	4	3						
705 - 736	X		х	X							
737 - 768	X		X	X	X						
769 - 800	X	X									
801 - 832	X	X			х						
833 - 864	X	X		X							
865 - 896	X	X		х	x						
897 - 928	x	Х	X								
929 - 960	х	Х	Х		х						
961 - 992	х	Х	Х	х							
993 -1024	X (NO	X T U	X SED)	X	X						

X = Switch in OPEN Position (Depressed to the Left)
Switches No. 1 and 2 should be in CLOSED Position

Notes

A group of 32 consecutive I/O points are required to be selected for this module to communicate to and from the Input Table. Each of the eight input channels will utilize these same 32 I/O points. Each time a total I/O scan is executed, one of the input channels of this module will be scanned, converted, and sent to the Input Table. The next time a total I/O scan is executed the channel number is incremented by one and the next channel is scanned, converted, and sent to the Input Table on the same 32 consecutive I/O points. This process is repeated each time a total I/O scan is executed until all of the input channels are converted and sent to the Input Table. At this time the process repeats, one per total I/O scan, until all the

channels are scanned. It is the responsibility of the user to program a store in a register of these data bits during the solution cycle and use the registers for reference inputs as the Input Table will reflect only one input channel at a time and the register will contain all eight channels.

Using, the CPU extended functions, the user can elect to read repeatedly the same channel or scan up to all eight channels in sequence at a much faster rate than the normal I/O scan rate. This is accomplished by programming a "DO I/O" function during the normal user program. Refer to the Application Guide Manual, GEK-25365, for more detailed instructions.

Electrical Installation

The Analog Input modules can be driven and wired to in many ways. A symbolic Analog Input module circuit is shown in Figure 2. For typical user input connections when using a 0 to 10V or -10 to +10V analog signal transmitter refer to Figure 3.

When using a +1 to +5V, 4 to 20 ma, or 10 to 50 ma analog signal transmitter refer to Figure 4 for typical user input connections. For other types of analog transmitters such as Type 2, Type 3, or Type 4, refer to Instrument Society of America Standard, ISA-S50.1, for guidance. A Type 2 transmitter is shown for reference only in Figure 5.

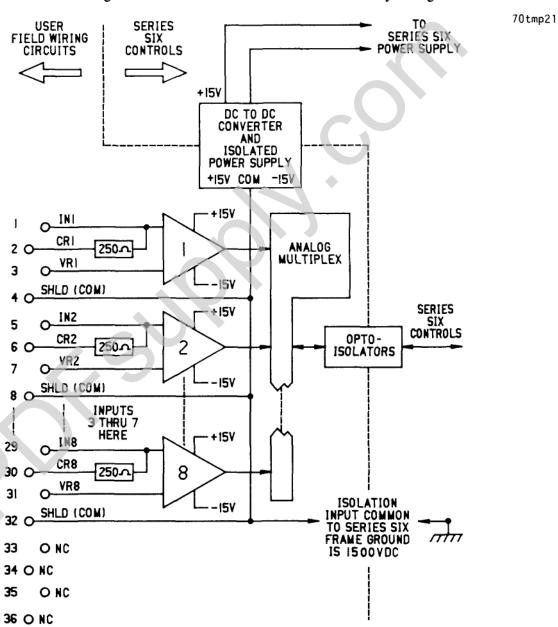


Figure 2. Symbolic Analog Input Module Circuit

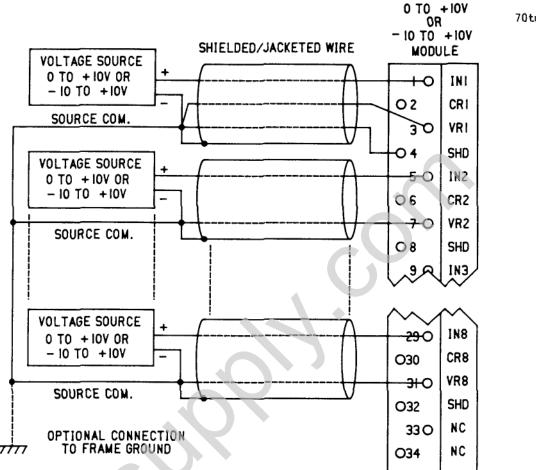


Figure 3. Typical User Input Connections (0 to +10 V or -10 to +10 V)

CAUTION

The signal lines (such as wires that connect to an IN- or VR- terminal) must be no more than \pm 11V from the common (SHD) terminal at any time or damage may result to the module.

Notes

For an unbalanced source, the ground shield should be connected to the source common or ground as shown in the connection for input 2 above. If all of the source inputs to this module come from the same location and are referenced to the same common, a connection is made as shown in Input 1 to the SHD/common input to the module.

If the inputs to any one analog input module come from multiple sources, care must be taken to connect each of the source common points together and then connect to the Analog Input module at only one terminal, such as Terminal 4 above. This will eliminate multiple grounding or ground loops which can cause false input data.

All terminals marked SHD/common are internally connected together with 1500 V DC isolation to chassis. An optional way to connect the shields is to connect only one end and connect all shields at the module. Source commons should all be connected together and connected back to the module at only one place such as Input 1 above.

70tmp22

70tmp23

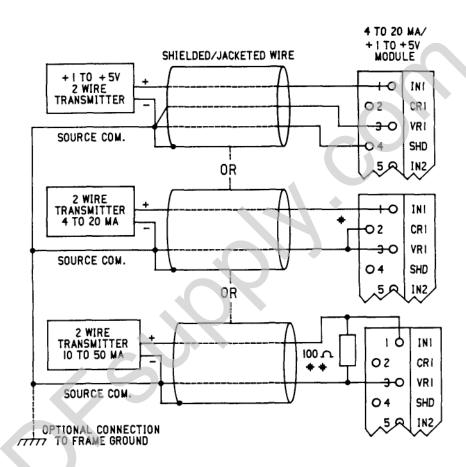


Figure 4. Typical User Input Connections (4 to 20 mA)

Notes

Reference the notes shown pertaining to Figure 3. These same notes apply to Figure 4, plus the following:

* 250 Ω shunt resistor provided internally (by GE Fanuc - NA) for 4 to 20 ma input use only.

** User may connect a 100 Ω shunt resistor if input is 10 to 50 ma.

Reference Figure 5 for a typical Type 2 current transmitter wiring hookup.

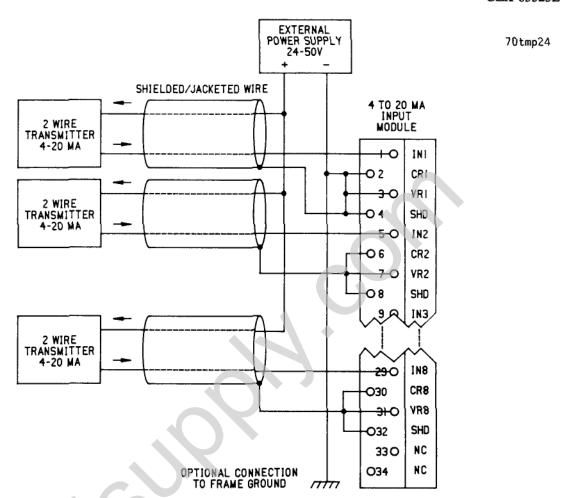


Figure 5. Typical Wiring for Type 2 Transmitters Requiring an External Power Supply (4 to 20 mA)

Notes

This configuration represents only one of many possible wiring hookups to the 4 to 20 ma input module and is shown for reference only. Using the symbolic Analog Input module circuit Figure 2, this Figure (5), and the Instrument Society of America Standard

ISA-S50.1 publication most wiring hookups will be readily apparent.

The signal lines (such as inputs to IN-, CR-, or VR-terminals) must be no more than 11 volts from the common (SHD) terminal at any time or damage may result to the module.

Digital Data Format

Bit 1 corresponds to the lowest input number in the group of 32 inputs reserved for the module; Bit 32 corresponds to the highest input number in this group.

Bits 1-8:	Channel Number: 8-bit binary number
	giving the number of the channel (1 to 8)
	being read. It consists of three signifi-
	cant bits (bits 1-3) and five zeroes (bits
	4-8).

Bit 13: Board OK: High if both +5 V and +15 V

power-supply levels are OK; LOW other-

wise.

Bits 9-16: Status Byte:

Bit 14:

Underrange: HIGH if input level is at or below the low end of the module range; LOW otherwise.

GEK-83525E			
Bit 9:	Valid Data: HIGH if the Board OK Light is On; LOW if the light is OFF. (Refer to Figure 2.)	Bit 15:	Overrange: HIGH if input level is at or above the high end of the module range; LOW otherwise.
Bit 10:	(Unused)	Bit 16:	Heartbeat: Changes state when the reading of a channel is complete.
Bit 11:	Open Wire: HIGH if input circuit is open (< 0.4 V) on channel being read, with the 4 to 20 mA/+1 to 5 V module; LOW otherwise. Should be ignored with the 0 to +10 V and -10 to +10 V modules.	Bits 17-24:	Data: Eight least significant of the 12 bits of data. Bit No. 17 is the least significant bit (LSB).
Bit 12:	Sign: HIGH with negative input to the -10 to +10 V module; LOW with positive input. Always LOW with the other modules.	Bits 25-32:	Data: Four most significant of the twelve bits of data (bits 25-28), plus four bits of sign extension (bits 29-32). Bit No. 28 is the most significant bit (MSB). The sign-extension bits all have the same value as the Sign bit (bit No. 12).

The 12 bits of data and the sign extension make up a 16-bit binary number which is in straight binary form for positive data, or 2's - complement form for negative data. For the bipolar (-10 to +10 V) module, bit No. 28 functions as a sign bit.

Table 3. Input Status Table

	Si	gn			Extension												Sta	tus				C	`har	mel	Nu	mb	er (Cod	e			
32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	
																																1
Sig	m			D	D	D	D	D	D	D	D	D	D	D	D	Н	0	U	В	s	0	N	v	z	z	z	z	Z	C	c	С	þ
	_	sion	ı	A	A	A	A	A	A	A	Α	Α	Α	Α	Α	Ε	v	N	0	I	P	0	Α	E	E	Ε	Е	E	н	Н	Н],
Bi				T	T	Т	Т	Т	Т	Т	Т	Т	Т	Т	Т	Α	Ε	D	Α	G	Ε	Т	L	R	R	R	R	R	A	Α	Α	1
				A	A	A	A	Α	Α	Α	Α	Α	Α	Α	Α	R	R	Ε	R	N	N		Ι	0	0	0	0	0	N	N	N	1
Bi	ts 2	9	4			i			1	1	l					T	R	R	D		l	U	D			l			N	N	N	1
thi	oug	gh		В	В	В	В	В	В	В	В	В	В	В	В	В	Α	R		В	w						1		E	Ε	E]
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Calibration Procedure

Required equipment:

Voltmeter - 5 digit (Voltage Source - Resolution to 0.1 MV) Calibration Connector (IC600MA508A) Calibration of the Analog Input module should be performed every 90 days. For maximum accuracy the board should be calibrated at the normal ambient temperature which occurs in operation.

1. SET UP:

Loosen the thumbscrews and remove the faceplate, taking care not to disturb the field wiring. Take the calibration connector, voltage source, and the voltmeter wired as shown in Figure 6 and connect it to the module in place of the faceplate.

Program the CPU to move the converted digital data from this channel to a convenient register location, where its value can be observed using the Program Development Terminal (PDT). This can be done by using the following program:

IWWW	W IXXXX IYYYY IZZZZ RAAAA
+/	+/+ I/O TO REG + ()
where:	
:wwww:	Lowest of the 32 consecutive input numbers used by this Analog Input module.
ixxxx:	Next input number: XXXX = WWWW + 1
IYYYY:	Next input number: YYYY = WWWW + 2
IZZZZ:	Input number corresponding to the LSB of digital input data: ZZZZ = WWWW + 16.
RAAAA:	Register to display digital input data.

2. LOW END:

Set the voltage source as close as possible to the value shown in Table 3.

Adjust R12 (Refer to Figure 1) until the digital output agrees with the value in Table 3. There will be a slight delay for the register output value to update to reflect an adjustment as eight (8) I/O total scans may be required to read and convert the data for channel 1.

3. HIGH END:

Set the voltage source as close as possible to the value in Table 3.

Adjust R11 (Refer to Figure 1) until the digital output matches the value in Table 3.

4. FINE ADJUSTMENT:

Repeat steps No. 2 and No. 3 until the module is in calibration at both ends of its range simultaneously.

Table 4. Module Calibration

	LOW	END	HIGH END						
MODULE	Source Voltage	Digital Output*	Source Voltage	Digital Output*					
0 to +10 V	+0.0024 V	00001	+9.9951 V	OFFE					
+1 to + 5 V (4 to 20 mA)	+1.0010 V	00001	+4.9980 V	OFFE					
-10 to +10 V	-9.9951 V	F801	+9.9902 V	07FE					

^{*}In Hexadecimal Format

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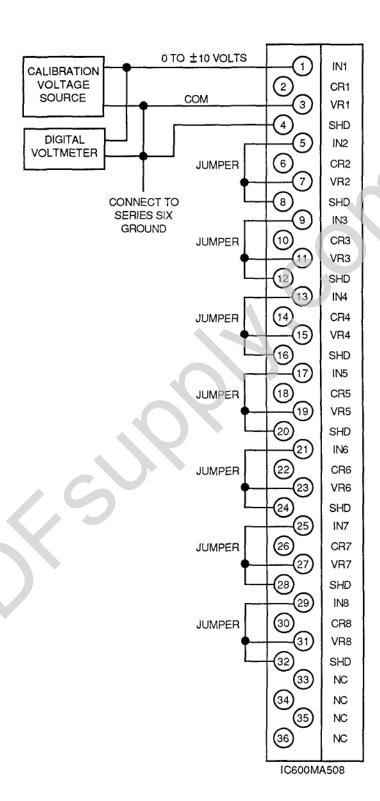


Figure 6. Wiring for Calibration of Analog Input Board

Module Reconfiguration

The Analog Input module can be reconfigured to function in any of three input ranges: 0 to +10V, -10

to +10V or 4 to 20 mA (1 to +5V). The user should note that all eight input channels, on any given module, provide a common input range.

INPUT RANGE	JUMPER POSITIONS
ov to $+10v$ (unipolar)	1-2, 7-10, 8-9, 6-11, 13-14, 21-22, 24-25
-l0V to +l0V (bipolar)	1-3, 4-7, 8-9, 6-11, 13-14, 22-23, 25-26
4 to 20 mA (+IV to 5V)	1-2, 3-4, 5-6, 7-8, 12-13, 21-22, 24-25

Table 5. Ordering Information

Module	Circuit Board & Faceplate	Faceplate
0 to $+10$ \mathbf{v}	IC600BF841	IC600FP841
-10 to +l0 v	IC600BF842	IC600FP841
4 to 20 mA (+1 to +5 V)	IC600BF843	IC600FP843

Calibration Connector IC600MMO8

The UL symbol on the nameplate means the product is listed by Underwriters Laboratories Inc. (UL Standard No. 508, Industrial Control Equipment, subsection Electronic Power Conversion Equipment.)

For further information, contact your local GE Fanuc sales office.

NOTE

The Analog Input module will function properly only when used with Series Six Plus CPUs and Models 60, 600 and 6000 CPUs with the following serial numbers:

Model 60 CPU: Cl88 -8135 -0130, and higher. Model 600 CPU: Cl88 -8138 -0100, and higher. Model 6000 CPU: Cl88 -8138 -6000, and higher.

If your CPU has a serial number lower than that listed, contact the PLC Product Service Specialist at (804)978 -5624 for assistance.